of a method of compensating for non-constant delay times of a network transmitting MPEG-2

and MPEG-4 data packets, comprising: said substeps of calculating an estimated jitter value

associated with a subsequent reference data packet comprises the substep of calculating a

corrected theoretical arrival time of a subsequent reference data packet based on said calculated

mean jitter value;

Such limitations, as recited in claims 4, 8, 13 and 16, are neither anticipated nor rendered

obvious by the art of record.

    With reference to claim 11, the prior art of record in combination with other claimed

limitations considered as a whole, neither teaches nor suggests the overall combination of a

method of compensating for non-constant delay times of a network transmitting MPEG-2 and

MPEG-4 data packets, comprising the steps of:

periodically receiving data packets with a nominal period:

detecting a clock-stamp reference value in a first reference data packet;

calculating a jitter value of each data packet received subsequent to said first reference data

packet until a second reference data packet having a clock-stamp reference value is detected;

determining a sample mean jitter from said jitter values;

establishing a corrected theoretical arrival time for said second reference data packet;

estimating the jitter of said second reference data packet; and

adjusting said clock-stamp reference value of said second reference data packet.

Such limitations, as recited in claim 11 is neither anticipated nor rendered obvious by the art of

record.

Regarding claim 12, the prior art of record in combination with other claimed limitations considered as a whole, neither teaches nor suggests the overall combination of an MPEG-2 and MPEG-4 transmission network comprising: a source device that transmits MPEG-2 or MPEG-4 data packets with a nominal period;

a destination device that receives said data packets; and

an electronic communication channel having a non-constant delay period that is coupled between said source device and said destination device to receive said data packets from said source device and provide said data packets to said destination device;

said destination device comprising an electronic controller that calculates a mean jitter value for a sample of said data packets, estimates a jitter value for a subsequent reference data packet outside of said sample, and adjusts a clock-stamp reference value of said subsequent reference data packet based on said estimated fitter value.

Such limitations, as recited in claim 12 is neither anticipated nor rendered obvious by the art of record

Claims 5, 6, 9, 10, 14, 15, 17 and 18 are allowed by virtue of their dependency to claims highlighted above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
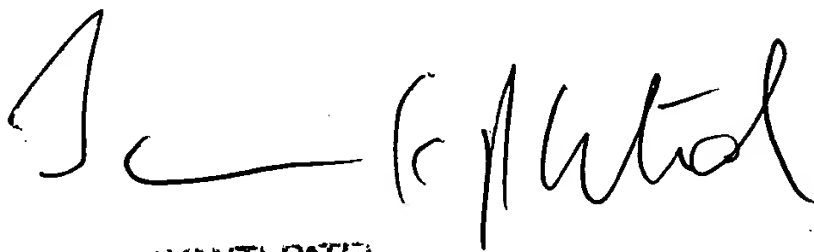
Conclusion

6.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Qutub Ghulamali whose telephone number is (571) 272-3014. The examiner can normally be reached on Monday-Friday from 8:00AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571) 272-2988. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Qutub Ghulamali.
January 5, 2005.

JAYANTI PATEL
SUPERVISORY PATENT EXAMINER